

Biography of GAURAV GANDHI

Gaurav Gandhi has completed his Bachelor of Engineering in Electronics and Communications from NSIT (formerly Delhi Institute of Technology, Delhi University) in 2002 where his thesis topic was "Experimentation with Field Programmable Analog Arrays". He subsequently joined STMicroelectronics as a Design Engineer where he was responsible for leading a team for design and development of a mixed signal chip for Multi standard On-Board-Unit system for Austrotrade la'italia. Autostrade is now largest Contract and On-Board-Unit Issuer for electronic tolling, with 5.3 million users and contractual agreements with over 600 banks in Italy. He has also served as an important member for know how transfer of an analog chip while serving as a Design engineer at ST-Grenoble.

Gaurav has joined Pazmany Peter Catholic University as a research associate in 2005 and subsequently was enrolled as a PhD student under the guidance of Prof. Tamas Roska. His PhD thesis is focussed on Electronic Implementation of Chaotic Circuits. During the course of his doctoral studies he has spent time as an intern/research associate at EPFL, Lausanne, UC Berkeley and Robotic Laboratory, PPKE.

He is currently working as a Senior Member of Technical Staff at Cadence Design Systems on development of a new cadence product.

Gaurav is the inventor of one US patent (Granted), One India patent and one Hungarian patent.