



# Kiss András

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## SZEMÉLYES ADATOK

Születési hely és idő: Gyula, 1982.

## MUNKAHELY

MTA-SZTAKI 2010 -

## ISKOLA

Doktori iskola: Pázmány Péter Katolikus Egyetem - Információs Technológia Kar 2007 -

Egyetem: Pannon Egyetem (Veszprémi Egyetem) - Műszaki Informatikai Kar - Műszaki Informatika Szak 2000 - 2007

## NYELV

Angol, középfok.

## ERedmények

OTDK különdíj 2007

### Lektorált nemzetközi folyóirat cikk:

Zs. Vörösházi, A. Kiss, Z. Nagy, P. Szolgay, „Implementation of embedded emulated-digital CNN-UM global analogic programming unit on FPGA and its application”, International Journal of Circuit Theory and Applications, Vol. 36, Issue 5-6, pp. 589-603, 2008, DOI: 10.1002/cta.507 (SCI index: 2.389)

Z. Nagy, L. Kék, Z. Kincses, A. Kiss, P. Szolgay, „Toward exploitation of cell multi-processor array in time-consuming applications by using CNN model”, International Journal of Circuit Theory and Applications, Vol. 36, Issue 5-6, pp. 605-622, 2008, DOI: 10.1002/cta.508 (SCI index: 2.389)

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Zs. Vörösházi, Z. Nagy, A. Kiss, and P. Szolgay, “An Embedded CNN-UM Global Analogic Programming Unit Implementation on FPGA,” in Proceedings of the 10th IEEE International Workshop on Cellular Neural Networks and their Applications, (Istanbul, Turkey), CNNA2006, August 2006.

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Z. Nagy, A. Kiss, S. Kocsárdi, M. Retek, Á. Csík, and P. Szolgay, “A Supersonic Flow Simulation on IBM Cell Processor Based Emulated Digital Cellular Neural Networks,” in Proc. of CMFF’2009, (Budapest, Hungary), pp. 502–509, 2009.

A. Kiss and Z. Nagy, “Computational Fluid Flow Simulation on Body Fitted Mesh Geometry with FPGA Based Emulated Digital Cellular Neural Networks,” in Proceedings of 12th International Workshop on Cellular Nanoscale Networks and their Applications, (Berkeley, CA, USA), CNNA2010, 2010.

L. Füredi, Z. Nagy, A. Kiss, and P. Szolgay, “An Improved Emulated Digital CNN Architecture for High Performance FPGAs,” in Proceedings of the 2010 International Symposium on Nonlinear Theory and its Applications, (Krakow, Poland), pp. 103–106, NOLTA2010, 2010.

Cs. Nemes, Z. Nagy, M. Ruszinkó, A. Kiss, and P. Szolgay, “Mapping of High Performance Data-Flow Graphs into Programmable Logic Devices,” in Proceedings of the 2010 International Symposium on Nonlinear Theory and its Applications, (Krakow, Poland), pp. 99–102, NOLTA2010, 2010.