

András Mozsáry

Professional Biography

András Mozsáry was born in Budapest, 31st May, 1979

He graduated from „Budapesti Piarista” High School in 1997

He has won 7th prize at the Hungarian National Physics Contest for High School Students (Fizika OKTV). As a consequence he was admitted to Budapest University of Technology and Economics (BUTE) without entrance exam.

He was ERASMUS exchange student in 2000 fall semester at Helsinki University of Technology. This time he participated in the „INSPECT” abbreviated research project of European Union. His personal task was the implementation of the 2.4GHz FM-DCSK modulated radio transmitter frontend, under the supervision of prof. Veikko Porra.

He has obtained M.Sc. degree in Electrical Engineering from BUTE in 2002. His major was Telecommunications. He has continued his studies as a Ph.D. student in the Multidisciplinary Technical Sciences Doctoral School of the Pazmany P. Catholic University

In autumn semester 2003 and spring semester 2004 he spent 2-2 months in Sevilla as a visiting researcher of Centro Nacional de Microelectrónica. With the help of prof. Angel Rodríguez-Vázquez, he has laid out the transistor-level architecture of the Hyperacuity Chip.

He spent the academic year 2004/05 in Taiwan where he has completed the Hyperacuity Chip design. The chip prototypes were manufactured at the nearby TSMC foundry.

In september 2006 András Mozsáry has joined the Analogic and Neural Computing Laboratory of MTA-SZTAKI. (Computer and Automation Research Institute of the Hungarian Academy of Sciences)